

SYSTEM AND METHOD FOR CONTROLLING LEAKAGE
CURRENT IN AN INTEGRATED CIRCUIT

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ABSTRACT OF THE DISCLOSURE

10 A circuit 10 is provided that comprises a source resistance transistor
12 connected to a common node 14. A word line driver circuit 18 receives
current if it is the word line driver selected from the V_{DD} supply voltage
through the source resistance transistor 12. The gate of source resistance
transistor 12 is connected to a bond pad 22 which can be alternatively
connected to the V_{DD} supply voltage through a bond pad 24 or to ground
potential through a bond pad 26. The effective threshold voltage of a
15 transistor 18 within driver 16 can be adjusted depending upon how the gate
of transistor 12 is connected. In this manner, a circuit can be adjusted to
compensate for process variation or to be more optimum for a selected
application by adjustment of the effective threshold voltage of selected
transistors.